

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	390	712/228.ccls	USPAT	OR	ON	2005/03/24 19:14
L2	472	712/245.ccls	USPAT	OR	ON	2005/03/24 19:14
L3	0	(partition\$4 with thread) with register with (reorder with buffer)	EPO; JPO	OR	ON	2005/03/24 19:14
L4	0	(partition\$4 with thread) with register with (reorder with buffer)	EPO; JPO	OR	ON	2005/03/24 19:14
L5	1	suspend with instruction with thread	EPO; JPO	OR	ON	2005/03/24 19:14
L6	1	(suspend near3 instruction) same thread	EPO; JPO	OR	ON	2005/03/24 19:14
L7	24	thread with suspend with instruction	USPAT	OR	ON	2005/03/24 19:14
L8	1	(thread with suspend with instruction) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
L9	24	(thread with switch with instruction) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
L10	17	(thread with (switch near3 instruction)) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
L11	8	(thread with operand with instruction) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
L12	15	(suspend near3 instruction) same thread	USPAT	OR	ON	2005/03/24 19:14
L13	511	(suspend near3 instruction)	USPAT	OR	ON	2005/03/24 19:14
L14	2	(suspend with instruction) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
L15	1	(suspend near3 instruction) and (thread with partition\$5)	USPAT	OR	ON	2005/03/24 19:14
S1	23	thread with suspend with instruction	USPAT	OR	ON	2004/09/28 17:54
S2	1	(thread with suspend with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 18:20
S3	20	(thread with switch with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/29 02:26
S4	1	(thread with resource with time with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 17:59

S5	0	(thread with operand with time with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 17:59
S6	8	(thread with operand with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 17:59
S7	13	(thread with (switch near3 instruction)) and (thread with partition\$5)	USPAT	OR	ON	2004/09/29 02:26
S8	0	("suspend instruction") and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 18:07
S9	0	(suspend near3 instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 18:07
S10	1	(suspend with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 18:07
S11	493	(suspend near3 instruction)	USPAT	OR	ON	2004/09/28 18:08
S12	14	(suspend near3 instruction) same thread	USPAT	OR	ON	2004/09/29 02:26
S13	1	(suspend with instruction) and (thread with partition\$5)	USPAT	OR	ON	2004/09/28 18:20
S14	50	(suspend) and (thread with partition\$5)	USPAT	OR	ON	2004/09/29 02:26
S15	9	("5933627" "6357016" "6496925" "6535905" "6493741" "5357617" "5584031" "5761522" "5961639").pn.	USPAT	OR	ON	2004/09/28 19:02
S16	0	"10039777".ap.	USPAT	OR	ON	2004/09/28 19:02
S17	0	"10039777".ap.	US-PGPUB	OR	ON	2004/09/28 19:03
S18	1	"039777".ap.	US-PGPUB	OR	ON	2004/09/28 20:21
S19	15	suspend with instruction with thread	US-PGPUB	OR	ON	2004/09/29 02:25
S20	5	(suspend with instruction with thread) and (partition\$4)	US-PGPUB	OR	ON	2004/09/28 20:46
S21	15	partition\$4 with thread with resource	USPAT	OR	ON	2004/09/29 02:25
S22	3	(partition\$4 with thread) with register with (reorder with buffer)	USPAT	OR	ON	2004/09/29 02:25
S23	12	"43193"	DERWENT	OR	ON	2004/09/29 00:03
S24	77	((suspend\$4 switch\$4) with thread) and (thread with partition\$5)	USPAT	OR	ON	2004/09/29 00:21
S25	368	712/228.ccls.	USPAT	OR	ON	2004/09/29 00:22
S26	461	712/245.ccls.	USPAT	OR	ON	2004/09/29 00:22
S27	0	(partition\$4 with thread) with register with (reorder with buffer)	EPO; JPO	OR	ON	2004/09/29 02:25

S28	0	(partition\$4 with thread) with register with (reorder with buffer)	EPO; JPO	OR	ON	2004/09/29 02:25
S29	0	partition\$4 with thread with resource	EPO; JPO	OR	ON	2004/09/29 02:25
S30	1	suspend with instruction with thread	EPO; JPO	OR	ON	2004/09/29 02:25
S31	1	(suspend) and (thread with partition\$5)	EPO; JPO	OR	ON	2004/09/29 02:26
S32	1	(suspend near3 instruction) same thread	EPO; JPO	OR	ON	2004/09/29 02:26
S33	0	(thread with (switch near3 instruction)) and (thread with partition\$5)	EPO; JPO	OR	ON	2004/09/29 02:26
S34	0	(thread with switch with instruction) and (thread with partition\$5)	EPO; JPO	OR	ON	2004/09/29 02:26